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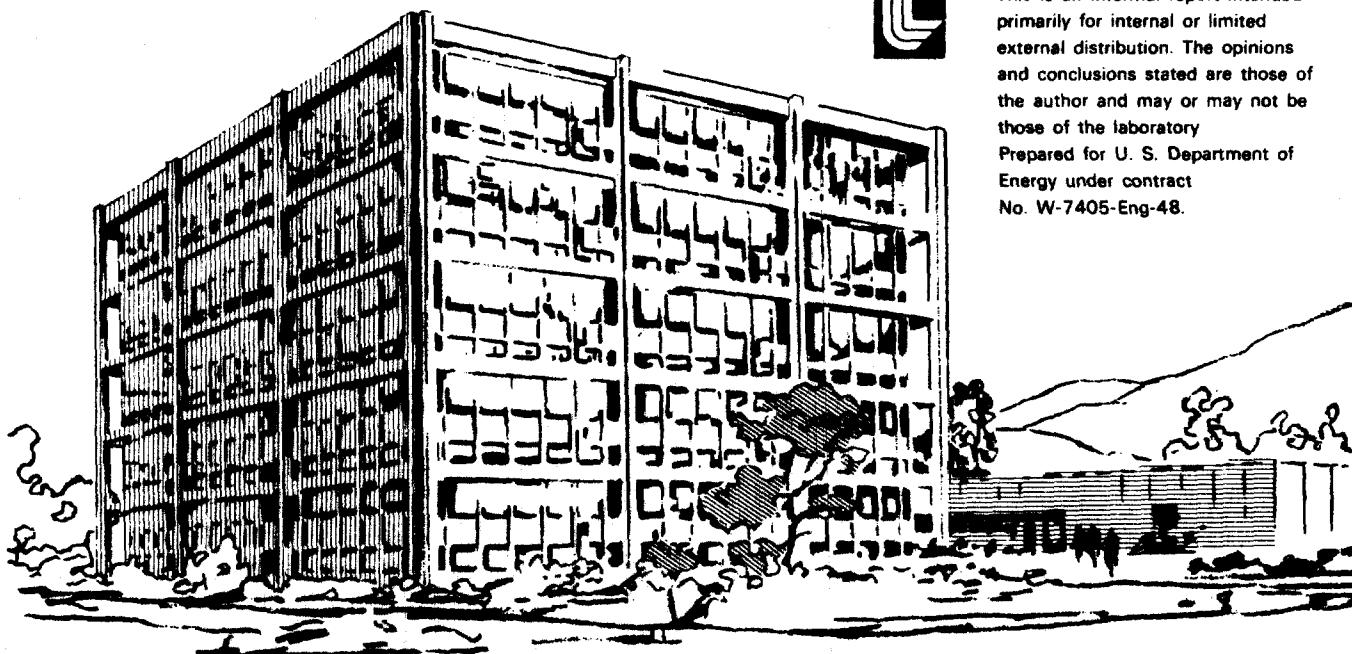
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REMOTE SYSTEM CONSOLE

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CONTENTS

Abstract	1
Introduction	1
Software Description	2
System Startup	2
I/O Request	2
Restriction	3
Software Installation	3
References	15

REMOTE SYSTEM CONSOLE

ABSTRACT

This report describes a simple method of implementing a remote system console on a remote computer using the Hewlett Packard DS/1000 communication software package on the HP 21MX-E series computer.

INTRODUCTION

The Hewlett Packard RTE-MII operating system^{1,2} requires a system console for logging all system-related messages. The LLL Seismic Network project found this console to be unacceptable for our unmanned remote computers, which are located in tunnels not easily accessible. This report describes a simple software package written in LLL SPL language. The package will transmit all messages destined for the system console back to the central computer, which is located in Livermore, California.

This report assumes that the reader is well-versed in using the RTE operating system.

SOFTWARE DESCRIPTION

The software package consists of four modules--DVA00, LSEND, LSTUQ, and STRTM. DVA00 is an I/O driver, which is a replacement for the HP-supplied terminal I/O driver. The driver will intercept all messages and pass them on to program LSEND.

LSEND will use the HP-supplied DS/1000^{3,4} subprogram DMESG to transmit the message back to the central computer.

SYSTEM STARTUP

The I/O driver DVA00 must be initialized before the first I/O write request. This is done by program STRTM when RTE-M starts running. The program will perform a control I/O call to logical unit (LU) 1 passing the location of the subsystem global area (SSGA) variable SSGLC.

SSGLC is a lock word used to synchronize the program LSEND and the I/O driver DVA00. The variable will contain the first word address (FWA) of the equipment table (EQT) entry for the driver when an I/O request is made. This variable will put the driver in a busy state until LSEND has transmitted the message to the central computer. The variable will be reset to zero after the message is transmitted to allow DVA00 to process the next request, if any.

I/O REQUEST

When a I/O request is performed, the RTE-M system will execute the standard I/O call procedure until it reaches driver DVA00. In the driver, SSGLC will be set to the first word address of the EQT entry for the driver. The driver will then schedule program LSEND to transmit the message to the central computer. The driver will then set the EQT word 4 for time-out processing and EQT word 15 for a time-out of 50 ms.

After the time-out, the driver is reentered in the continuation/completion section. If SSGLC is not zero, EQT word 15 is reset to the 50-ms time-out value, and a continuation exit is taken. If SSGLC is zero, EQT word 15 is set to zero and a completion exit is taken.

If the driver is busy (SSGLC ≠ 0), all I/O requests will be either buffered or queued.

RESTRICTION

This software package will not run under any RTE operating system with a dynamic memory mapping system, because the data buffer may not be mapped in with the program LSEND. The message length must not be over 72 bytes long.

SOFTWARE INSTALLATION

To install the software package perform the following steps:

1. Add module %DVA00 to the relocation of the system modules.
2. Set the buffer limits to 128,256.
3. Assign DVA00 to EQT# 2 as follows:

25,DVA00, B

The select code should be one that is not assigned to any device.

4. For device reference table (DRT) entry, enter 2,0 for logical unit 1.
5. Add module %LSTUQ to the relocation section of SSGA.
6. Relocate program LSEND as a regular application program.
7. Modify the startup program, STRTM, to pass the location of variable SSGLC to driver DVA00.

See Figs. 1 through 5 for detailed listing of modules and installation examples.

```

0001
0002     NAME DVA00 (0) "SYSTEM MESSAGE PROCESSOR DRIVER"
0003
0004     . . . SYSTEM MESSAGE PROCESSOR DRIVER. PUT EQT LOCATION INTO
0005     LMESG, THEN SCHEDULE LSEND TO PROCESS AND SEND
0006     THE MESSAGE TO CENTRAL
0007
0008     ASSEMBLE ["EXT $LIST"]
0009
0010     LET LSEND (3) BE INTEGER   I CONTAIN NAME OF MESSAGE TRANSMITTER MODULE
0011     LET EQT1 BE INTEGER      I EQT FVA
0012     LET EQT4 BE INTEGER      I USE TO SET TIME-OUT BIT
0013     LET EQT6 BE INTEGER      I USE FOR EQUEST CODE CHECK
0014     LET EQT7 BE INTEGER      I BUFFER ADDRESS
0015     LET EQT8 BE INTEGER      I BUFFER LENGTH
0016     LET EQT15 BE INTEGER     I TIME-OUT COUNTER
0017     LET I BE INTEGER
0018     LET TIOUT BE INTEGER
0019     LET SSGLC BE INTEGER
0020
0021     INITIALIZE LSEND TO "LSEND "
0022     INITIALIZE EQT1 TO 1660K
0023     INITIALIZE EQT4 TO 101663K
0024     INITIALIZE EQT6 TO 101665K
0025     INITIALIZE EQT7 TO 101666K
0026     INITIALIZE EQT8 TO 101667K
0027     INITIALIZE EQT15 TO 101774K
0028     INITIALIZE TIOUT TO -50
0029     INITIALIZE SSGLC TO 0
0030
0031     . . . INITIALIZATION, IF MESSAGE BUFFER IS NOT BUSY,
0032     MOVE EQT1 LOCATION TO BUFFER AND SCHEDULE LSEND.
0033
0034 IA00: SUBROUTINE GLOBAL, DIRECT
0035     IF ($EQT6 AND 3) = 3 THEN [SSGLC - $EQT7; $SSGLC - 1; GO TO IA20]
0036     IF ($EQT6 AND 3) # 2 THEN [.A. - 1; RETURN]
0037     IF SSGLC = 0 THEN [.A. - 4; RETURN]
0038
0039     . . . SAVE EQT LOCATION IN LOCK WORD FOR LSEND PROGRAM
0040
0041     $SSGLC - $EQT1
0042     ASSEMBLE ["JSB $LIST"; \
0043         "OCT 201"; \
0044         "DEF LSEND"]
0045
0046 IA20:
0047     SEQT15 - TIOUT
0048     SEQT4 - $EQT4 OR 10000K
0049     .A. - 0
0050     RETURN
0051     END IA00
0052
0053
0054     . . . ENTER CONTINUATION SECTION BECAUSE OF TIME OUT,
0055     CHECK TO SEE IF UNLOCK. IF NOT CONTINUE TO WAIT
0056
0057
0058 CA00: SUBROUTINE GLOBAL, DIRECT
0059
0060     IF $SSGLC # 0 THEN \
0061         [SEQT15 - TIOUT; \
0062             ASSEMBLE ["ISZ CA00"]; \
0063             RETURN]
0064
0065
0066     . . . CLEAR TIME-OUT
0067
0068     SEQT15 - 0
0069     .A. - 0
0070     RETURN
0071     END CA00
0072 END DVA00

```

FIG. 1. Listing of I/O driver DVA00.

```

0001      NAME LSEND (17, 40) "SYSTEM MESSAGE PROCESSOR"
0002  !
0003  ! . . . FORM MESSAGE AND SEND IT TO CENTRAL
0004  !
0005  !
0006  LET DMESG BE SUBROUTINE, EXTERNAL
0007  LET EXEC BE SUBROUTINE, EXTERNAL
0008  LET SSGLC BE INTEGER, EXTERNAL
0009  LET CNODE BE INTEGER      ! CENTRAL CPU NODE NUMBER
0010  LET CT BE INTEGER        ! MESSAGE LENGTH
0011  !
0012  ! INITIALIZE CNODE TO 99
0013  !
0014  LSEND:
0015  !
0016  !
0017  CT = $(SSGLC + 7)
0018  IF CT < -72 THEN CT = -72
0019  IF CT > 36 THEN CT = 36
0020  !
0021  ! . . . TRANSMIT MESSAGE TO CENTRAL
0022  !
0023  L20:
0024  CALL DMESG (CNODE, $$ (SSGLC + 6), CT)
0025  GO TO L40 ! ERROR RETURN
0026  !
0027  ! . . . CLEAR LOCK
0028  !
0029  L30:
0030  SSGLC = 0
0031  !
0032  CALL EXEC (6)
0033  !
0034  !
0035  ! . . . SAVE CONTENT OF A AND B REGISTERS
0036  !
0037  !
0038  L40:
0039  A = .A.
0040  B = .B.
0041  ! GO TO L20
0042  ! GO TO L30
0043  END LSEND

```

FIG. 2. Listing of LSEND.

```
SSG1      NAME LSTUQ (38) "SYSTEM MESSAGE PROCESSOR SSGA"  
SSG2      |  
SSG3      | . . . LOCK WORD AND CONTAIN LOCATION OF EQT TABLE FWA  
SSG4      |  
SSG5      | WHEN NON-ZERO  
SSG6      |  
SSG7      LET SSGLC BE INTEGER, GLOBAL  
SSG8      INITIALIZE SSGLC TO 8  
SSG9      END LSTUQ
```

FIG. 3. Listing of LSTUQ.

```

0001
0002     NAME STRTM (17, 1) "RTE-M START-UP PROGRAM"
0003
0004     ! . . . THIS PROGRAM INITIALIZE DS/1000 BY SCHEDULING LSTEN
0005     ! AND PASSING A BUFFER WHICH CONTAINS ANSWERS FOR THE
0006     ! INITIALIZATION SEQUENCE. AFTERWARD LSTEN AND THIS
0007     ! PROGRAM ARE KILL BY CLEARING THE ID SEGMENT
0008
0009     LET EXEC BE SUBROUTINE, EXTERNAL
0010     LET POPEN BE SUBROUTINE, EXTERNAL
0011     LET PREAD BE SUBROUTINE, EXTERNAL
0012     LET ICONV BE SUBROUTINE, EXTERNAL
0013
0014     LET BUF (80) BE INTEGER          ! BUFFER FOR ANSWER DATA FOR LSTEN
0015     LET LEN BE INTEGER             ! WORD LENGTH OF ARRAY BUF
0016     LET CLASS BE INTEGER          ! CLASS NUMBER
0017     LET LSTEN (3) BE INTEGER        ! LSTEN PROGRAM NAME
0018     LET RECLEN BE INTEGER          ! RECORD LENGTH OF EACH ANSWER IN BUF
0019     LET SSGLC BE INTEGER, EXTERNAL
0020     LET MESSS BE FUNCTION, EXTERNAL
0021     LET DMM (3) BE INTEGER
0022     LET LNODE BE INTEGER, EXTERNAL
0023     LET TAGI (20) BE INTEGER, EXTERNAL
0024     LET CPARM (3) BE INTEGER
0025     LET PCB (4) BE INTEGER
0026     LET RSYN (3) BE INTEGER
0027     LET DCOFF (144) BE INTEGER, EXTERNAL
0028     LET ALIGN (3) BE INTEGER
0029     LET CAL (3) BE INTEGER
0030     LET MESS (16) BE INTEGER
0031
0032     ! INITIALIZE BUF TO "YES      ", \ 1000 CONNECTED?
0033     "NO      ", \ 3000 CONNECTED?
0034     "/D      ", \ # OF ACTIVE TRANSACTION
0035     "2      ", \ ENABLED LU #
0036     "/E      ", \ LAST LU
0037     "0      ", \ NOT FILE NOT BEING USED
0038     "1      ", \ LOCAL CPU #
0039     "3      ", \ NUMBER OF NODES
0040     "1      ", \ CPU #, LU, TIMEOUT
0041     "99,2,30", \ CPU #, LU, TIMEOUT
0042     "201,2,30", \ CPU #, LU, TIMEOUT
0043     "OPRM   ", \
0044     "/E      ", \
0045     "SY      ", \ SECURITY CODE
0046     "/T      ", \ TIME ADJUSTMENT
0047     "200   ", \
0048     "100   ", \
0049     "10    ", \
0050     "0    ", \
0051     "/E      ", ! ALL DONE
0052
0053     ! INITIALIZE LEN TO 80
0054     ! INITIALIZE CLASS TO 0
0055     ! INITIALIZE LSTEN TO "LSTEN "
0056     ! INITIALIZE RECLEN TO 4
0057     ! INITIALIZE CPARM TO "CPARM "
0058     ! INITIALIZE NODE TO 99
0059     ! INITIALIZE RSYN TO "RSYN "
0060     ! INITIALIZE ALIGN TO "ALIGN "
0061     ! INITIALIZE CAL TO "CAL "
0062     ! INITIALIZE MESS TO "DS/1000 ERROR ", 3(0), " FROM STRTM "
0063     ! INITIALIZE DMM TO "DMM "
0064
0065     STRTM:
0066
0067     ! . . . PUT NODE NUMBER INTO LSTEN BUFFER TO INITIALIZE NODE
0068
0069     BUF (25), BUF (33) = LNODE + 30060K

```

FIG. 4. Example of RTE-MII startup program.

```

0070  !
0071  ! . . . INITIALIZE DVAB# BY GIVING IT LOCATION OF SSGA VARIABLE SSGLC
0072  !
0073      CALL EXEC (3, 1, @SSGLC)
0074  !
0075  ! . . . PUT DATA INTO CLASS VIA WRITE/READ
0076  !
0077      CALL EXEC (20, 0, BUF, LEN, 0, 0, CLASS)
0078  !
0079  ! . . . SCHEDULE LSTEN WITH THREE PARAMETERS TO PERFORM CLASS GET
0080  !
0081      CALL EXEC (9, LSTEN, -1, CLASS, RECLEN)
0082  !
0083  !
0084      SSGLC = 0 ! DS/1000 IS INITIALIZE, RELEASE LOCK
0085  !
0086  !
0087  ! . . . GET STARTUP PARAMETERS FROM CENTRAL
0088  !
0089      TAGI (1) = LNODE
0090      CALL POPEN (PCB, IERR, CPARM, NODE, TAGI)
0091      IF IERR # 0 THEN GO TO ERROR
0092  !
0093  ! . . . GET CALIBRATION FACTORS
0094  !
0095      TAGI (1) = LNODE
0096      CALL PREAD (PCB, IERR, DCOFF, 96, TAGI)
0097      IF IERR # 0 THEN GO TO ERROR
0098  !
0099  ! . . . NEED TO PERFORM ALIGNMENT?
0100  !
0101      IF TAGI (1) = 0 THEN GO TO L2000
0102  !
0103 L1000:
0104      CALL EXEC (9, ALIGN)
0105  !
0106  ! . . . NEED TO PERFORM CALIBRATION?
0107  !
0108 L2000:
0109      IF TAGI (2) = 0 THEN GO TO L3000
0110  !
0111      CALL EXEC (9, CAL, 66)
0112  !
0113  ! . . . TIME SYN
0114  !
0115 L3000:
0116      CALL EXEC (9, RSYN)
0117  !
0118  ! . . . START DMM
0119  !
0120      CALL EXEC (10, DMM)
0121  !
0122      CALL EXEC (6)
0123  !
0124  ! . . . ERROR
0125  !
0126 ERROR:
0127      CALL ICONV (IERR, MESS (8))
0128      CALL EXEC (2, 1, MESS, 16)
0129      TAGI (1), TAGI (2) = 0
0130      GO TO L1000
0131      END STRTM

```

FIG. 4. (continued)

```

0001 GEN
0002 OUTPUT ON P#1000
0003 MAP MODULES,GLOBALS ON "R#100
0004 END
0005 *
0006 * TYPE OF SYSTEM?
0007 2
0008 *
0009 * TBG CHNL?
0010 15
0011 *
0012 * PRIV. INT?
0013 12
0014 *
0015 * LWAM?
0016 77777
0017 *
0018 * FWA BP?
0019 21
0020 *
0021 *
0022 * CHANGE ENTS?
0023 .MPY,RP,100200
0024 .DIV,RP,100400
0025 .DLD,RP,104200
0026 .DST,RP,104400
0027 .FAD,RP,105000
0028 .FSB,RP,105020
0029 .FMP,RP,105040
0030 .FDV,RP,105060
0031 .IFIX,RP,105100
0032 .FLOAT,RP,105120
0033 .MBT,RP,105765
0034 .MVW,RP,105777
0035 *
0036 * . . . SELECT CODE FOR DVR62
0037 *
0038 ADSCC,AB,14
0039 ADSCD,AB,13
0040 END
0041 *
0042 * REL SYS MODS
0043 LINKS IN CURRENT
0044 REL XMSY2R::11
0045 REL XMSC2::11
0046 REL XMEX2::11
0047 REL XMDI2::11
0048 REL XMIO2::11
0049 REL XMBU::11
0050 REL XMTI::11
0051 REL XMTS::11
0052 *
0053 *
0054 REL XMOP::11
0055 REL XMCL::11
0056 REL XMRN::11
0057 REL XMDMI::11
0058 REL XMER::11
0059 REL XDVA00::11
0060 REL XDVA65::11
0061 REL XDVA62::11
0062 END
0063 *

```

FIG. 5. Answer file example for generating RTE-MII system with remote system console.

```

0064 * # OF I/O CLASSES?
0065 32
0066 *
0067 * # OF RESOURCE NUMBERS?
0068 32
0069 *
0070 * BUFFER LIMITS (LOW,HIGH)?
0071 128,256
0072 *
0073 *
0074 * EQT TBL
0075 *
0076 *
0077 * . . . EQT 1 - COMMUNICATION BOARD
0078 *
0079 10,DVA65,X=7,T=i6
0080 *
0081 * . . . EQT 2 - SYSTEM MESSAGE PROCESSOR
0082 *
0083 25,DVA80,B
0084 *
0085 *
0086 *
0087 * . . . EQT 3 - UI CARD FOR ADC, ETC.
0088 *
0089 13,DVA62,T=50J
0090 *
0091 END
0092 *
0093 * DRT TBL
0094 *
0095 * . . . LU 1 - SYSTEM MESSAGE PROCESSOR
0096 *
0097 2,0
0098 *
0099 * . . . LU 2 - COMMUNICATION CARD
0100 *
0101 1,0
0102 *
0103 * . . . LU 3 - UI CARD FOR ADC. ETC.
0104 *
0105 3,0
0106 *
0107 END
0108 *
0109 * INT TBL
0110 *
0111 *
0112 *
0113 * . . . COMMUNICATION CARD
0114 *
0115 10,PRG,QUEUE
0116 *
0117 * . . . UI ADC DATA CHANNEL
0118 *
0119 13,EQT,3
0120 *
0121 * . . . UI ADC COMMAND CHANNEL
0122 *
0123 14,EQT,3
0124 *
0125 *
0126 END
0127 *
0128 *

```

FIG. 5. (continued)

```

#129 * #ID SEG?
#130 20
#131 *
#132 * START-UP PRG?
#133 *
#134 STRTM
#135 *
#136 * REL RES LIB
#137 REL XRLIB#(.ENTR)::11
#138 REL XMALRN::11
#139 END
#140 *
#141 * REL SSGA
#142 REL XDSLB1(RES)::11
#143 REL XDSLB1(#REQU)::11
#144 REL XDSLB1(DRTEQ)::11
#145 REL XDSLB1(PGMAD)::11
#146 REL XLSTUQ::11
#147 REL XRSSGA::11
#148 REL XLNOD1::11
#149 END
#150 *
#151 * # WDS IN COMM?
#152 0
#153 *
#154 * ALIGN AT NEXT PAGE?
#155 YES
#156 *
#157 *
#158 *
#159 * . . . DS/1000 PROGRAM QUEUE
#160 *
#161 REL XQUEUE::11
#162 REL XMRNRQ::11
#163 END
#164 *
#165 0
#166 *
#167 * . . . DS/1000 PROGRAM GRPM
#168 *
#169 REL XGRPM::11
#170 REL XMRNRQ::11
#171 END
#172 *
#173 0
#174 *
#175 * . . . DS/1000 PROGRAM RTRY
#176 *
#177 REL XRTTY::11
#178 END
#179 *
#180 0
#181 *
#182 * . . . DS/1000 PROGRAM QCLM
#183 *
#184 REL XQCLM::11
#185 SEARCH XMSYLB::11
#186 SEARCH XRLIB#::11
#187 END
#188 *
#189 0
#190 *
#191 * . . . DS/1000 PROGRAM UPLIN
#192 *
#193 REL XUPLIN::11

```

FIG. 5. (continued)

```

0194 REL %MRNRQ:::11
0195 REL %$MMES:::11
0196 END
0197 *
0198 /
0199 *
0200 *
0201 *
0202 * . . . SYSTEM MESSAGE PROCESSOR MODULE
0203 *
0204 REL %LSEND:::11
0205 REL %MRNRQ:::11
0206 REL %MDRCT:::11
0207 REL %MREIO:::11
0208 REL %MCNMO:::11
0209 SEARCH %DSLBB:::11
0210 SEARCH %DSLBI:::11
0211 SEARCH %RLIB#:::11
0212 END
0213 *
0214 /
0215 *
0216 *
0217 * . . . APPLICATION PROGRAMS START
0218 * HERE
0219 *
0220 *
0221 *
0222 *
0223 *
0224 * . . . DS/1000 OPERATOR REQUEST MONITOR MODULE
0225 *
0226 REL %OPERM:::11
0227 REL %$MMES:::11
0228 SEARCH %DSLBI:::11
0229 END
0230 *
0231 /
0232 *
0233 *
0234 *
0235 *
0236 *
0237 * . . . FREE PERIOD INITIATOR
0238 *
0239 REL %PERID:::11
0240 END
0241 *
0242 /
0243 *
0244 * . . . S T R 6 2
0245 *
0246 REL %STR62:::11
0247 REL %PACKT:::11
0248 SEARCH %MSYLB:::11
0249 SEARCH %RLIB#:::11
0250 END
0251 *
0252 /
0253 *
0254 *
0255 * . . . R C E M
0256 *
0257 REL %RCEM:::11
0258 REL %ICONV:::11

```

FIG. 5. (continued)

```

0259 SEARCH XDSLBB:::11
0260 SEARCH XDSLBB:::11
0261 SEARCH XDSLBB:::11
0262 SEARCH XMSYLB:::11
0263 SEARCH XRLIB#:::11
0264 END
0265 *
0266 #
0267 *
0268 *
0269 * . . . D M M
0270 *
0271 REL XDMM:::11
0272 SEARCH XMSYLB:::11
0273 REL XDMWA:::11
0274 END
0275 *
0276 #
0277 *
0278 * . . . RTEM START UP PROGRAM
0279 *
0280 REL XSTRTM:::11
0281 REL XICONV:::11
0282 SEARCH XDSLBB:::11
0283 SEARCH XDSLBB:::11
0284 SEARCH XDSLBB:::11
0285 SEARCH XMSYLB:::11
0286 SEARCH XRLIB#:::11
0287 END
0288 *
0289 #
0290 *
0291 * . . . TIME SYNC
0292 *
0293 REL XRSYN:::11
0294 REL XICONV:::11
0295 SEARCH XDSLBB:::11
0296 SEARCH XDSLBB:::11
0297 SEARCH XDSLBB:::11
0298 SEARCH XMSYLB:::11
0299 SEARCH XRLIB#:::11
0300 END
0301 *
0302 #
0303 *
0304 *
0305 * . . . DC OFFSET CALCULATOR
0306 *
0307 REL XDCCAL:::11
0308 END
0309 *
0310 #
0311 *
0312 *
0313 * . . . CALIBRATION
0314 *
0315 REL XCAL:::11
0316 REL XCALI:::11
0317 REL XCALFN:::11
0318 REL XCALDI:::11
0319 REL XSTORA:::11
0320 REL XCDIGF:::11
0321 REL XFP3:::11
0322 REL XCALDS:::11
0323 REL XICONV:::11

```

FIG. 5. (continued)

```

#324 REL XSQUEZ:::11
#325 SEARCH XDSLB3:::11
#326 SEARCH XDSLB2:::11
#327 SEARCH XDSLB1:::11
#328 SEARCH XMSYLB:::11
#329 SEARCH XRLIB#:::11
#330 REL XCALBU:::11
#331 END
#332 *
#333 #
#334 *
#335 * . . . ALIGNMENT
#336 *
#337 REL XALIGN:::11
#338 END
#339 *
#340 #
#341 *
#342 *
#343 *
#344 *
#345 * . . . DS/1000 PROGRAM LSTEN TO INITIALIZE DS/1000
#346 *
#347 REL XLSTEN:::11
#348 SEARCH XDSLB3:::11
#349 REL XREADF:::11
#350 REL XRWSUB:::11
#351 REL XRWND$:::11
#352 REL XPPASF:::11
#353 SEARCH XMSYLB:::11
#354 SEARCH XRLIB#:::11
#355 END
#356 *
#357 #
#358 *
#359 END
#360 *
#361 * . . . SET LWAM
#362 *
#363 70777
#364 *
#365 * ALIGN AT NEXT PAGE?
#366 YES
#367 *
#368 * SNAPSHOT?
#369 SNAP ON &R#100
#370 END
#371 *

```

FIG. 5. (continued)

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WTF